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APPLICATION FILING OF GRP ART	
NUMBER 371(e) DATE UNIT FIL FEE REC'D ATTY.DOCKET.NO TOT CLAIMS IND CLAIR	.IMS]
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CONFIRMATION NO. 8208
CORRECTED FILING RECEIPT

23389 SCULLY SCOTT MURPHY & PRESSER, PC 400 GARDEN CITY PLAZA SUITE 300 GARDEN CITY, NY 11530

Date Mailed: 10/16/2007

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections

# Applicant(s)

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# **Assignment For Published Patent Application**

NEC Corporation, Tokyo, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 23389

## Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP04/15159 10/14/2004

# Foreign Applications

JAPAN 2003-434028 12/26/2003

# If Required, Foreign Filing License Granted: 04/09/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US 10/584,736** 

Projected Publication Date: Not Applicable

Non-Publication Request: No

Early Publication Request: No

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### Title

Optical input substrate, optical output substrate, optical input/output substrate, a fabrication method for these substrates, and an optical element integrated semiconductor integrated circuit

# **Preliminary Class**

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